

an insulating film covering the first and second gate lines and the first and the second data lines;

a pixel electrode disposed in the pixel region, the pixel electrode overlapping one of the first and the second regions of the first data line, the pixel electrode overlapping one of the first and second regions of the second data line such that one of the first and second regions of the first data line and one of the first and second regions of the second data line are not overlapped by the pixel electrode; and

a switching element disposed in the pixel region and connected between the second gate line and the pixel electrode.

12. (Amended) A method of manufacturing a liquid crystal display device, comprising the steps of:

providing a substrate;

forming first and second gate lines formed on the substrate;

forming first and second data lines intersecting the first and second gate lines so as to define a pixel region, wherein each one of the first and second data lines has longitudinally separated first and second regions;

forming an insulating film covering the first and second gate lines and the first and the second data lines;

forming a switching element disposed in the pixel region and connected between the second gate line and the pixel electrode; and

forming a pixel electrode in the pixel region to overlap one of the first and the second regions of the first data line, and to overlap one of the first and second regions

of the second data line such that one of the first and second regions of the first data line and one of the first and second regions of the second data line are not overlapped by the pixel electrode.

18. (Amended) The method of claim 13, wherein the pixel electrode extends over the first region of the first data line and extends over the second region of the second data line.

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the subject application. The Final Office Action of June 18, 2002 has been received and contents carefully reviewed.

Claims 1-22 are pending. Claims 1, 12, and 18 have been amended. Applicant gratefully acknowledges the allowability of claims 2, 7, 8, 13, and 19 if rewritten in independent form including all of the limitations of the base claims and any intervening claims.

As a preliminary matter, Applicant submits herewith a copy of the request for priority and cover page, and a copy of the postcard, which show that the certified copy of the priority document was filed on April 23, 2001. Applicant respectfully requests the USPTO to acknowledge foreign priority under 35 USC § 119.

The Examiner rejected claims 1 and 12 under 35 USC § 102(e) as being anticipated by Lee (US Patent No. 6,226,057). Applicant respectfully traverses this rejection.

Independent claim 1 recites a combination of elements including a pixel electrode disposed in the pixel region, the pixel electrode overlapping one of the first and the second